

## **AMENDMENTS TO THE SPECIFICATION**

**Please replace the paragraph beginning at page 1, line 9, with the following rewritten paragraph:**

Since the start of digital television broadcasting ~~on~~in December 2000, demand for digital television BS/CS tuners ~~is~~has been increasing year after year.

**Please replace the paragraph beginning at page 3, line 22, with the following rewritten paragraph:**

Figure 14 is a diagram illustrating the conventional variable gain amplification circuit, wherein 101 denotes a variable gain mixer using a variable output load impedance, 102 denotes an RF signal source, 103 denotes an LO signal source, 104 denotes an output terminal, 106 denotes a control circuit, and 112 denotes a capacitor.

**Please replace the paragraph beginning at page 6, line 21, with the following rewritten paragraph:**

Accordingly, it is possible to attenuate unnecessary signals such as an interference wave signal, an intermodulation distortion, an RF leakage signal, and an LO leakage signal by setting the capacitance C12 so that the cutoff frequency  $f_c$  becomes a maximum frequency that does not attenuate a desired IF signal. As a result, degradation in distortion characteristics can be suppressed.

**Please replace the paragraph beginning at page 7, line 3, with the following rewritten paragraph:**

However, in the construction of the conventional variable gain amplification circuit shown in figure 15, since the variable output load impedance 114 of the variable gain mixer 101 varies during gain control, the cutoff frequency  $f_c$  or the resonance frequency  $f_r$  varies, whereby unnecessary signals such as an interference wave signal, an intermodulation distortion, an RF leakage signal, and an LO leakage signal cannot be sufficiently attenuated, resulting in degradation in distortion characteristics of the variable gain mixer 101 or the latter block.

**Please replace the paragraph beginning at page 18, line 21, with the following rewritten paragraph:**

While in this first embodiment the variable resistor 7 is a digital variable resistor comprising resistors and switches connected in parallel as shown in figure 3, the present invention is not restricted thereto. The variable resistor 7 may be constituted by ~~every element~~ elements or circuit circuits that can control the resistance, such as a variable resistor comprising resistors and switches connected in series, a variable resistor comprising resistors and switches connected in parallel and resistors and switches connected in series, or a variable resistor that can vary resistance components of a transistor.